



Notes:

- 1). Circuitry shown outside dotted line is chassis mounted.
- 2). Circuitry shown inside dotted line is board mounted.
- 3). This board also contains Low Batt Disconnect circuitry shown on another page.
- 4). Circuit supply voltage sources shown on Low Batt Disconnect Schematic.
- 5). All resistors are 1/4w unless otherwise noted.
- 6). 1RY1 shown de-energized (normal operating condition, commercial 115 present).
- 7). Timer Line Sense Module relay shown energized (normal operating condition, commercial 115 present).

DRAWING TITLE		1A1 Timing Control Schematic Diagram	
LOCATION 116 S. Prairie Ave. Waukegan, IL. 60085			
PROJECT NO.	001	DRAWN BY:	John R. Schaefer 17 Aug 97
SCALE:	None	DOC NAME:	Timing Control Scat.drw
Power Backup System	DOC LOCATION:		G:\John\Power Backup
	DRAWING NO.		SCH-001-002 4 REV